Notice of Allowability	Application No.	Applicant(s)
	10/635,628	HIRA ET AL.
	Examiner	Art Unit
	David Lam	2818
The MAILING DATE of this communication apper All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this ap or other appropriate communication IGHTS. This application is subject t	plication. If not included not will be mailed in due course. THIS
1. This communication is responsive to		
2. The allowed claim(s) is/are <u>1-7</u> .		
3. The drawings filed on <u>04 December 2003</u> are accepted by the Examiner.		
<ul> <li>4.</li></ul>		
Attachment(s)  1. ☑ Notice of References Cited (PTO-892)  2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)  3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date	6. ☐ Interview Summary Paper No./Mail Da 98), 7. ☐ Examiner's Amend	te ment/Comment
4. Examiner's Comment Regarding Requirement for Deposit of Biological Material	8. ⊠ Examiner's Statem 9. □ Other	DAVID LAM PRIMARY EXAMINER

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## Allowable Subject Matter

1. The following is an examiner's statement of reasons for allowance: Claims 1-7 are allowable over the prior art of record because none of the prior art whether taken singularly or in combination, especially when these limitations are considered within the specific combination claimed, to teach: a semiconductor memory device comprising: a data latch circuit, among others as claimed in independent claim 1, which latches output data when the signal of an aforementioned selective output line pair is not a complementary signal level and outputs first data or second data corresponding to the complementary signal level, either a first or a second complementary signal level, indicated by the signal level of the aforementioned selectable output line pair.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

## Conclusion

- 2. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.
  - Son (6,026,045) discloses a semiconductor memory device having multibank.

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to David Lam whose telephone number is 571-272-1782. The examiner can normally be reached on 7:00-3:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Nelms can be reached on 571-272-1787. The fax phone numbers for the organization where this application or proceeding is assigned is (703) 872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

D. Lam

DAVID LAM
PRIMARY EXAMINER

September 6, 2004